

Abstract of the Disclosure

5 A switch controlling circuit for the testing
and fine-tuning of integrated circuits comprising of a
series of flip-flops chain together in a serial manner.
The contents of the flip-flop are shift in from the input
of the first flip-flop in the chain. The output of each
flip-flop connects to individual switch whereby the
states of the flip-flops control the state of the
10 switches.